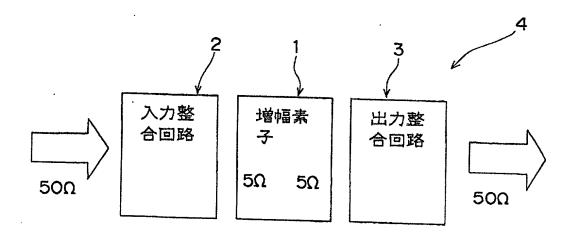
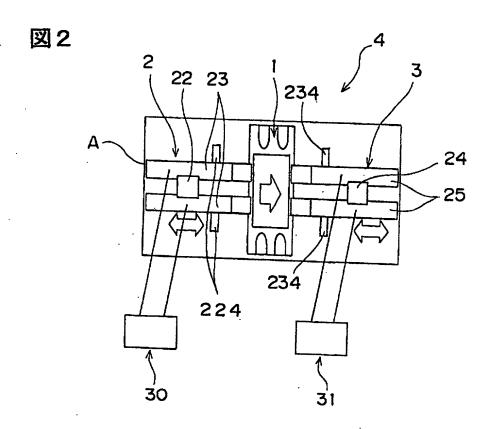
図 1





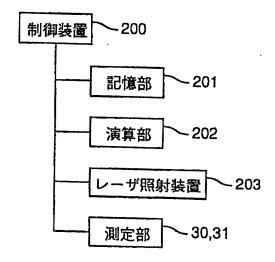
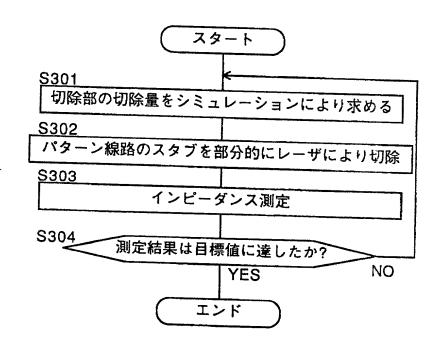
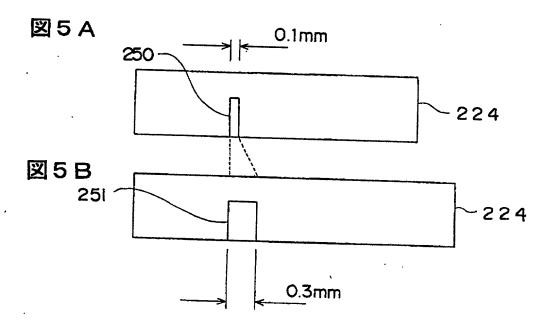
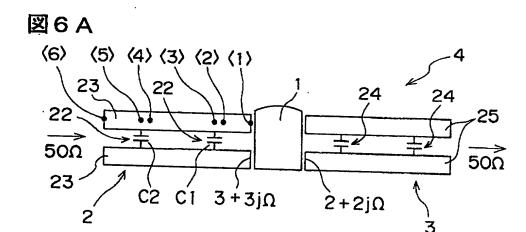
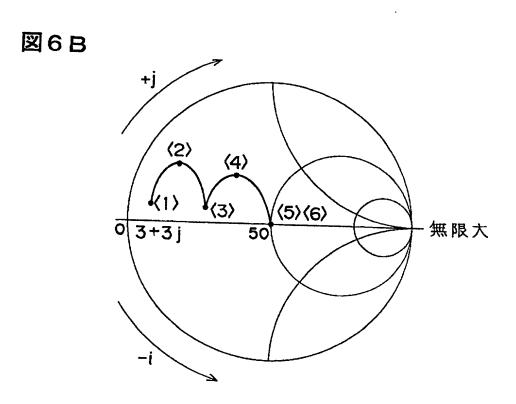


図 4

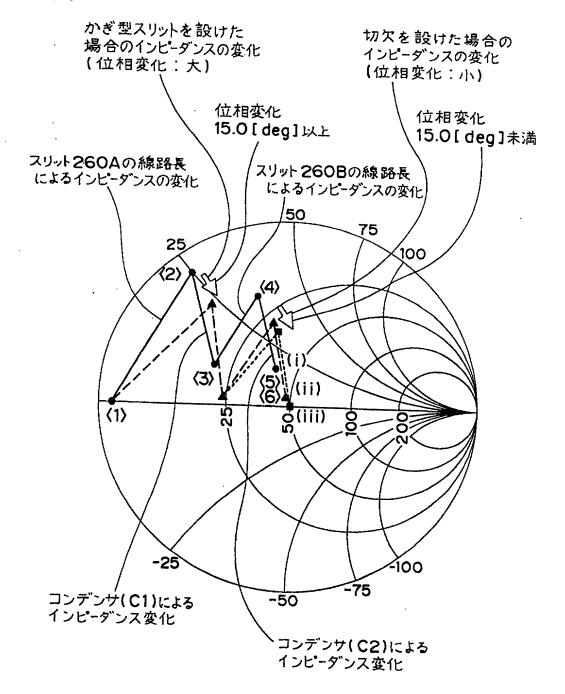








## 図7



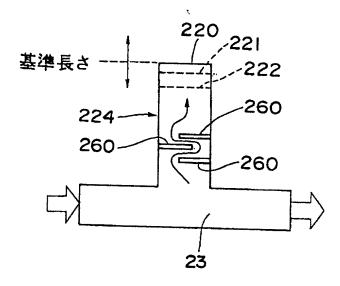


図9

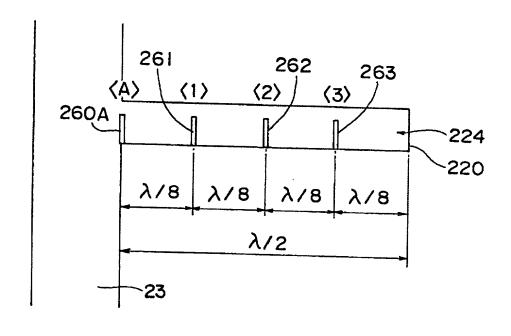


図10

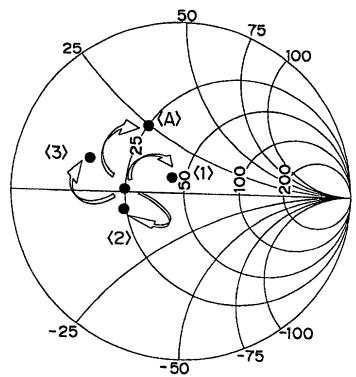


図11

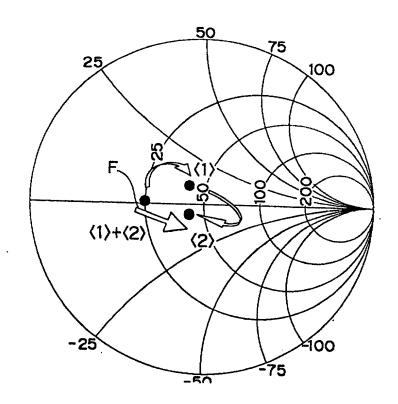


図12

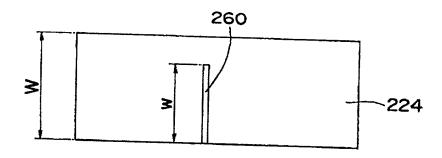


図13

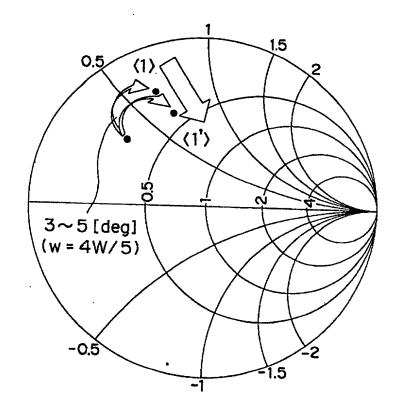


図14

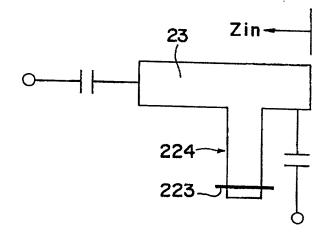


図15

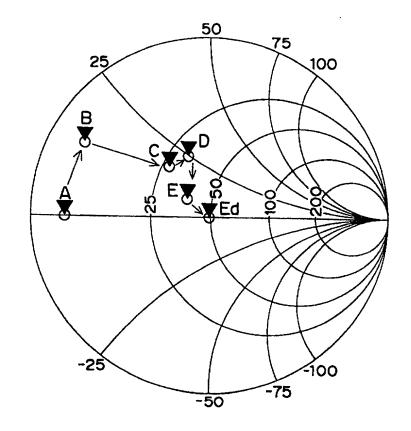


図16

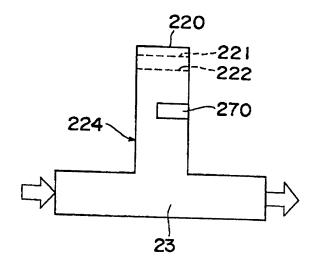


図17

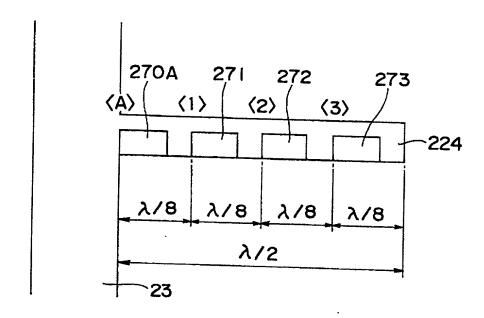


図18

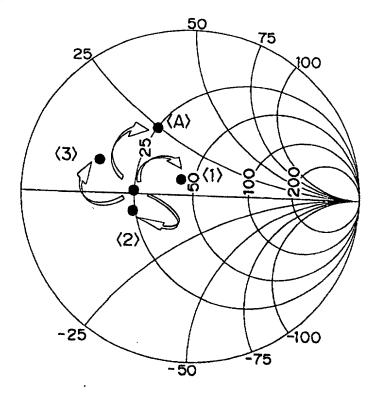


図19

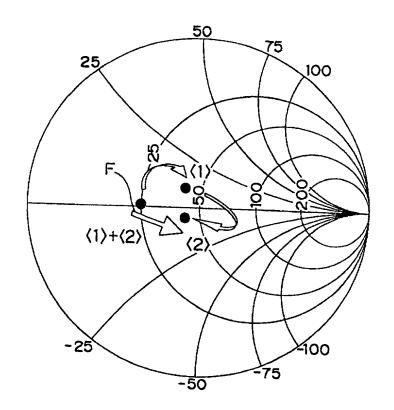
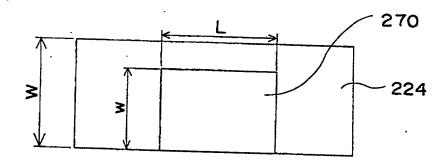


図20



## 図21

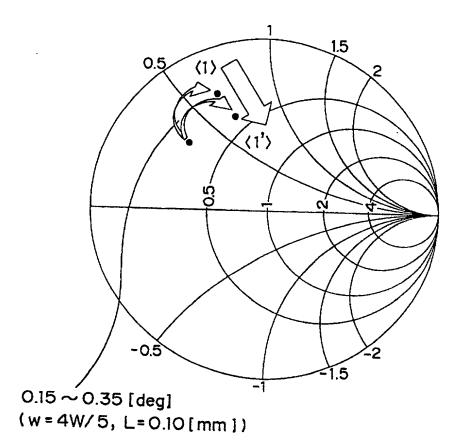


図22

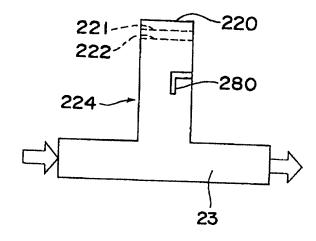


図23

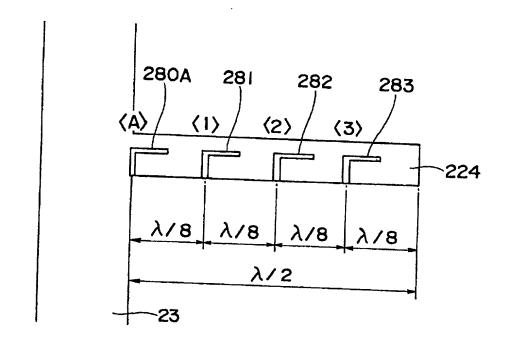


図24

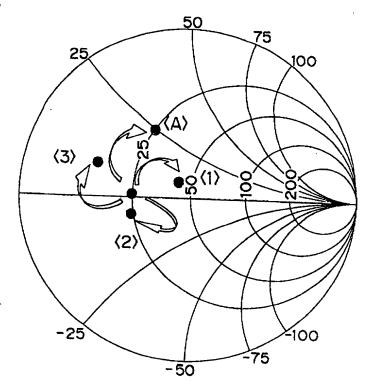


図25

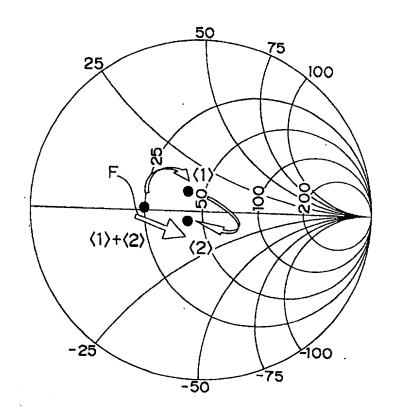


図26

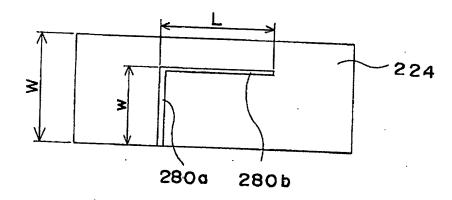


図27

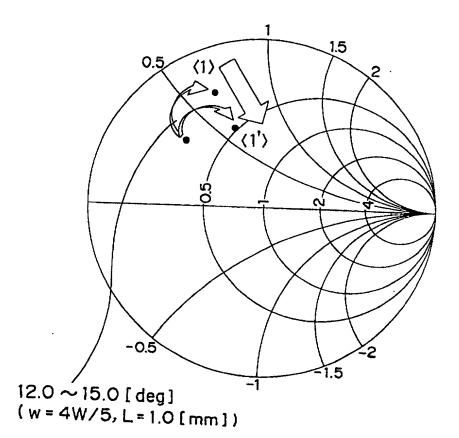


図28

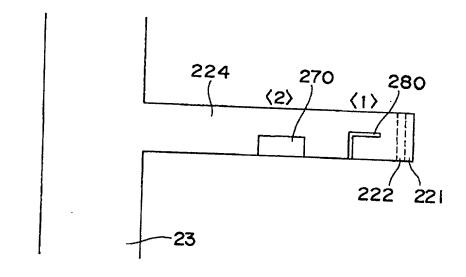
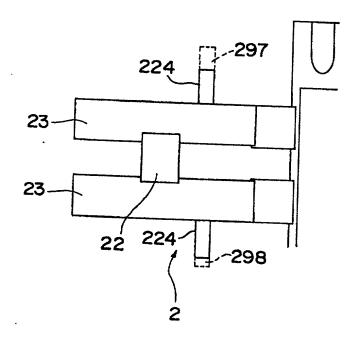
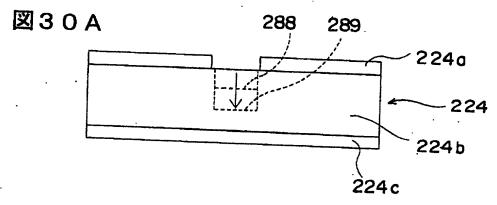


図29





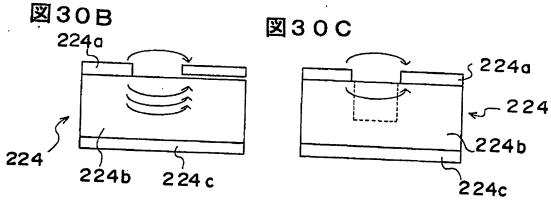
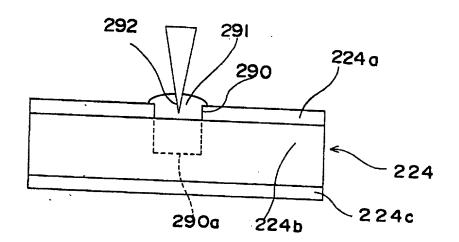
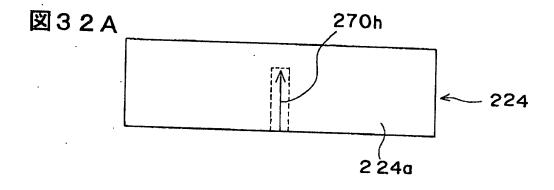
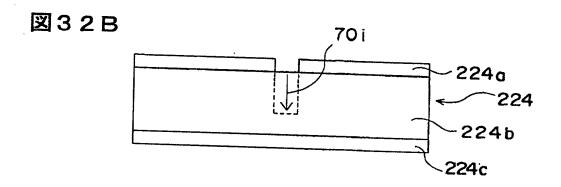


図31







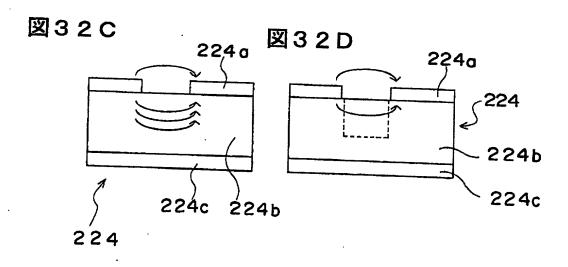


図33

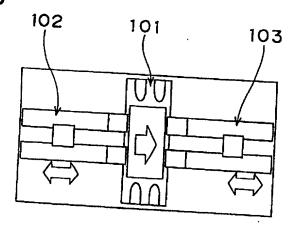


図34

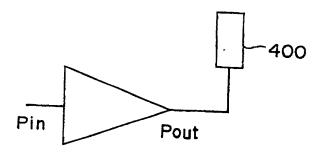


図35

